

U.S. Application No. 09/889,459
Reply to Office Action dated November 18, 2005

PATENT
450106-02850

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application. An identifier indicating the status of each claim is provided.

Listing of Claims

1. (Currently Amended) A digital signal processing apparatus, comprising:
~~a plurality of digital signal processing blocks and a host arithmetic operation processing block as functions necessary for processing a digital signal for controlling entire of said digital signal processing apparatus through outputting a command that does not depend on hardware and that is not on real time basis;~~
~~a plurality of digital signal processing blocks each having means for interpreting said command from said host processing block and for controlling a hardware component;~~
~~a bus for connecting said host arithmetic operation processing block and said plurality of digital signal processing blocks; and~~
~~an interface for an extension function providing medium connected to said bus, wherein the extension function providing medium has: comprises~~
~~-means for accomplishing an extension function, [[;]] and~~
~~-a script embedding a command for operating the extension function, and~~
~~wherein, when the extension function providing medium is attached to said bus through said interface, the script is sent to said host arithmetic operation processing block side and a function of the extension function providing medium is operated corresponding to the command embedded in the script.~~

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2. (Cancelled)

3. (Cancelled)

4. (Original) The digital signal processing apparatus as set forth in claim 1,

wherein the command is described and embedded in a script of hypertext,
wherein the hypertext is interpreted by a browser and a picture for operating the extension function is displayed, and

wherein a command corresponding to the function is embedded and displayed in the picture for operating the extension function.

5. (Currently Amended) A digital signal processing system, comprising:
a digital signal processing apparatus ~~having comprising:~~
~~a plurality of digital signal processing blocks and a host arithmetic operation~~
processing block as functions necessary for processing a digital signal ~~for controlling entire of~~
~~said digital signal processing apparatus through outputting a command that does not depend on~~
~~hardware and that is not on real time basis,~~

a plurality of digital signal processing blocks each having means for interpreting
said command from said host processing block for controlling a hardware component,

a bus for connecting said host arithmetic operation-processing block and said
plurality of digital signal processing blocks, and

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an interface for an extension function providing medium connected to said bus;
and
an extension function providing medium attached to said interface of the
extension function providing medium on the digital signal processing side,
wherein said extension function providing medium ~~has~~comprises:
-means for accomplishing an extension function, [();] and
-a script embedding a command for operating the extension function, and
wherein, when said extension function providing medium is attached to said bus
through said interface, the script is sent to said host arithmetic operation processing block side
and a function of said extension function providing medium is operated corresponding to the
command embedded in the script.

6. (Cancelled)

7. (Cancelled)

8. (Original) The digital signal processing system as set forth in claim 5,
wherein the command is described and embedded in a script of hypertext,
wherein the hypertext is interpreted by a browser and a picture for operating the
extension function is displayed, and
wherein a command corresponding to the function is embedded and displayed in
the picture for operating the extension function.

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9. (Currently Amended) An extension function providing method,
comprising the steps of:

structuring functions necessary for processing a digital signal as a plurality of
digital signal processing blocks and a host arithmetic operation processing block;

controlling a digital signal processing apparatus through a command that does not
depend on hardware and that is not on real time basis;

interpreting said command and controlling a hardware component;

connecting the host arithmetic operation processing block and the plurality of
digital signal processing blocks through a bus; and

providing an interface for an extension function providing medium connected to
the bus,

wherein the extension function providing medium has:comprises

-means for accomplishing an extension function, [[;]] and

-a script embedding a command for operating the extension function, and

wherein, when the extension function providing medium is attached to the bus
through the interface, the script is sent to the host arithmetic operation processing block side and
a function of the extension function providing medium is operated corresponding to the
command embedded in the script.

10. (Cancelled)

11. (Cancelled)

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12. (Original) The extension function providing method as set forth in
claim 9,

wherein the command is described and embedded in a script of hypertext,
wherein the hypertext is interpreted by a browser and a picture for operating the
extension function is displayed, and

wherein a command corresponding to the function is embedded and displayed in
the picture for operating the extension function.